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## A Memory Benchmarking Characterisation of ARM-Based Systems-on-Chip for Data Stream Computing

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The computing requirements of Big Science are ever-increasing and the volume of data produced by some of its projects has long exceeded available off-line storage capacity, necessitating the use of innovative means of data capturing. The volume of data expected to be produced by projects such as the Square Kilometre Array and the to-be upgraded Large Hadron Collider will far exceed existing system capacities. In addition, system and energy costs are sources of increasing concern. A potential solution involves the use of low-cost, low-power ARM-based Systems-on-Chip in large arrays in a manner which provides massive parallelisation for the purposes of high data throughput or Data Stream Computing. This focus on throughput rates increases the importance of memory performance to overall system performance. Using three memory performance benchmarks (namely STREAM, LMBench and pmbw) the performance of three ARM-based Systems-on-Chip, namely the Cortex-A7, -A9 and -A15, is characterised. The implications of these results in the context of Data Stream Computing are then discussed.

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